




**Examination Routine for Diploma in Computer Science and Engineering (WBSCTE Syllabus)  
Odd Semester: Theory Sessional and Practical Internal**

Date of Examination	Subject Code	Subject Name	Subject Type / Component	From	To
08-09-2017	CST/3/304	Data Structure	Theory	10:00AM	12:00 PM
08-09-2017	CST/3/303	Digital Logic Design	Theory	12:20PM	02:20PM
08-09-2017	CST/3/304	Data Structure	Practical	02:40PM onwards	
11-09-2017	CST/3/302	C Programming	Theory	10:00AM	12:00AM
11-09-2017	CST/3/301	Professional Practice-I(PC Maintenance)	Practical	12:20PM	03:20PM
11-09-2017	CST/3/302	C Programming	Practical	03:30PM onwards	
12-09-2017	CST/3/305	Computer Organization & Architecture	Theory	10:00AM	12:00AM
12-09-2017	CST/3/303	Digital Logic Design	Practical	12:20PM onwards	
13-09-2017	CST/3/306	Electronic Devices & Circuits	Theory	10:00AM	12:00AM
13-09-2017	CST/3/306	Electronic Devices & Circuits	Practical	12:20PM	3:20PM
13-09-2017	CST/3/PP-I	Discrete Mathematics	Theory	03:40PM	5:40PM

 6/9/17  
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